

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of October 2, 2003

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

D4 **Claim 1 (Currently Amended):** A head assembly comprising:

a mounting surface;

an integrated circuit chip which is mounted on the mounting surface and processes signals;

and

a head slider which is provided with a head and is mounted on the mounting surface,

said integrated circuit chip being covered by a layer 2 μ m or thinner,

a height of the integrated circuit chip, including the layer, being lower than a height of the head slider from the mounting surface.

Claim 2 (Original): The head assembly as claimed in claim 1, wherein said layer covering the integrated circuit chip is formed by evaporation.

Claim 3 (Original): The head assembly as claimed in claim 2, wherein said layer is made of poly(p-xylylene).

Claim 4 (Withdrawn): The head assembly as claimed in claim 1, wherein said integrated

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of **October 2, 2003**

4
Can't
circuit chip has a first surface provided with conductor bumps, and a second surface opposite to the first surface, wherein a peripheral portion of the second surface is chamfered.

Claim 5 (Currently Amended): The head assembly as claimed in claim 1, wherein said layer covers at ~~test~~ least peripheral portions of the integrated circuit chip.

Claim 6 (Withdrawn): The head assembly as claimed in claim 1, wherein said layer is made of a low-viscosity resin selected from a group consisting of photo-curing resins including ultraviolet-curing resins, and thermosetting resins.

Claim 7 (Canceled)

Claim 8 (Currently Amended): A disk unit for reading information from and writing information to a disk, comprising:

a head assembly having a mounting surface, a head slider provided with a head and mounted on the mounting surface, and an integrated circuit chip which is mounted on the mounting surface and processes information read from and/or written to the disk via the head,

said integrated circuit chip being covered by a layer 2 μ m or thinner,

a height of the integrated circuit chip, including the layer, being lower than a height of the head slider from the mounting surface.

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of October 2, 2003

D4
cont

Claim 9 (Withdrawn): The disk unit as claimed in claim 8, wherein said layer covers at least peripheral portions of the integrated circuit chip, and said layer is made of a low-viscosity resin selected from a group consisting of photo-curing resins including ultraviolet-curing resins, and thermosetting resins.

Claim 10 (Withdrawn): A semiconductor part comprising:

a main chip body; and

a covering layer formed by evaporation and covering the main chip body.

Claim 11 (Withdrawn): The semiconductor part as claimed in claim 10, wherein a portion of the main chip body is exposed via the covering layer.

Claim 12 (Withdrawn): The semiconductor part as claimed in claim 10, wherein said main chip body has an upper surface, a lower surface and peripheral side surfaces, and further comprising:

an integrated circuit formed on the lower surface of the main chip body;

conductor bumps formed on the lower surface of the main chip body;

an under-filling layer formed on the lower surface of the main chip body, so that tip ends of the conductor bumps are exposed via the underfilling layer.

D4
Can H
Claim 13 (Withdrawn): The semiconductor part as claimed in claim 12, further comprising:

alignment marks provided on the lower surface of the main chip body on outer sides of the integrated circuit,

said under-filling layer having holes exposing the alignment marks.

Claim 14 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, comprising the steps of:

- (a) forming a first layer on first and second surfaces of a wafer which has conductor bumps formed on one of the first and second surfaces located at opposite sides of the wafer;
- (b) dicing the wafer having the first layer into a plurality of chips; and
- (c) forming a second layer on peripheral side surfaces of each of the diced chips.

Claim 15 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, comprising the steps of:

- (a) forming a first layer on a first surface of a wafer which has conductor bumps formed on a second surface of the wafer located at an opposite side from the first surface;
- (b) dicing the wafer having the first layer into a plurality of chips in a state where the wafer is adhered on a film, without cutting the film;
- (c) separating the film from only peripheral portions of each of the diced chips; and

D4
Cont
(d) forming a second layer on each of the chips in a state where the peripheral portions of the diced chips are separated from the film.

Claim 16 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, comprising the steps of:

(a) adhering a film on a surface of a wafer which has conductor bumps formed thereon, so as to surround portions on the surface of the wafer, including the conductor bumps, which later become chips by a waved shape of the film;

(b) dicing the wafer having the film into a plurality of chips; and

(c) forming a layer on each of the diced chips.

Claim 17 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, said integrated circuit chip having a main chip body and a layer covering the main chip body, said method comprising the steps of:

(a) supplying a predetermined amount of resin which has a surface tension smaller than a wetting with respect to the main chip body from a nozzle onto an upper surface of the main chip body, said predetermined amount being larger than an amount of the resin required to form the layer;

(b) moving the nozzle near the upper surface of the main chip body to a height corresponding to a thickness of the layer which is to be formed; and

(c) sucking resin by a suction force smaller than the wetting with respect to the main chip

body, so as to remove excess resin, to thereby form the layer which covers the main chip body.

D4
Con

Claim 18 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, said integrated circuit chip having a main chip body, conductor bumps and a layer covering the main chip body, said method comprising the steps of:

(a) forming the layer on the main chip body and the conductor bumps by evaporation, in a state before crushing pointed tip ends of the conductor bumps;

(b) pushing the conductor bumps against a surface to simultaneously crush the pointed tip ends of the conductor bumps and make the layer covering the pointed tip ends to recede, to thereby expose surfaces at the crushed portions of the conductor bumps.

Claim 19 (Withdrawn): A method of producing an integrated circuit chip which is mounted on and forms a part of a head assembly, said integrated circuit chip having a main chip body, conductor bumps and a layer covering the main chip body, said method comprising the steps of:

(a) adhering a mold release agent on tip ends of the conductor bumps;

(b) forming the layer on the entire main chip body including the conductor bumps by evaporation; and

(c) removing portions of the layer on the tip ends of the conductor bumps having the mold release agent interposed there between, to thereby expose surfaces of the tip ends of the conductor

DF
Con 17
bumps.

Claim 20 (Withdrawn): A bonding apparatus for bonding conductor bumps on a semiconductor chip onto corresponding pads on a substrate by ultrasonic vibration, comprising:

a stage having a receiving surface for receiving a first surface of the substrate opposite to a second surface of the substrate provided with the pads; and

a bonding unit having an end surface for holding a first surface of the semiconductor chip opposite to a second surface of the semiconductor chip provided with the conductor bumps, and a closing member having a closing surface,

said end surface adhering the first surface of the semiconductor chip by suction via a suction hole which opens at the end surface,

said closing member being movable to close the suction hole at the end surface, so that the closing surface and the end surface form a single flat surface.

Claim 21 (Withdrawn): The bonding apparatus as claimed in claim 20, wherein said bonding unit comprises:

a transport mechanism which transports the semiconductor chip to a position above the stage.

Claim 22 (Withdrawn): The bonding apparatus as claimed in claim 21, wherein said bonding unit further comprises:

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of **October 2, 2003**

4
D
Can 4
a pressing mechanism which presses the semiconductor chip against the substrate which is placed on the stage in a state where the closing member closes the suction hole.

Claim 23 (Withdrawn): The bonding apparatus as claimed in claim 22, further comprising:
an ultrasonic radiator provided on the pressing mechanism and subjecting the semiconductor chip to ultrasonic vibration when bonding the bumps on the corresponding pads.

Claim 24 (Withdrawn): The bonding apparatus as claimed in claim 22, wherein said transport mechanism and said pressing mechanism are independent mechanisms.

Claim 25 (Withdrawn): The bonding apparatus as claimed in claim 22, wherein said transport mechanism and said pressing mechanism are formed by a single mechanism.

Claim 26 (Withdrawn): The bonding apparatus as claimed in claim 22, further comprising:
an ultrasonic radiator provided on the stage and subjecting the substrate to ultrasonic vibration when bonding the bumps on the corresponding pads.

Claim 27 (Withdrawn): The bonding apparatus as claimed in claim 20, wherein the end surface has an area greater than an area of the first surface of the semiconductor chip.

24
Can't

Claim 28 (Withdrawn): A bonding method for bonding conductor bumps on a semiconductor chip onto corresponding pads on a substrate by ultrasonic vibration, comprising the steps of:

(a) placing the substrate on a stage having a receiving surface for receiving a first surface of the substrate opposite to a second surface of the substrate provided with the pads;

(b) holding and transporting the semiconductor chip to a bonding position above the stage by a bonding unit, said bonding unit having an end surface for holding a first surface of the semiconductor chip opposite to a second surface of the semiconductor chip provided with the conductor bumps by suction via a suction hole which opens at the end surface, and having a movable closing member having a closing surface which forms a single flat surface together with the end surface when the closing member is moved to close the suction hole at the end surface;

(c) pressing the semiconductor chip against the substrate by the end surface in a state where the suction hole at the end surface is closed by the closing member; and

(d) bonding the conductor pads to the corresponding pads by ultrasonic vibration.

Claim 29 (Withdrawn): The bonding method as claimed in claim 28, wherein said step (d) subjects one of the semiconductor chip and the substrate to ultrasonic vibration when bonding the bumps on the corresponding pads.

Claim 30 (Previously Presented): The head assembly as claimed in claim 1, wherein said

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of **October 2, 2003**

D4
conf
layer covers at least an entire upper surface of the integrated circuit chip.

Claim 31 (Withdrawn): The head assembly as claimed in claim 1, wherein said layer covers upper and side surfaces of the integrated circuit chip.

Claim 32 (Withdrawn): The head assembly as claimed in claim 1, further comprising:
an under-filling filling a gap between a lower surface of the integrated circuit chip and the mounting surface,

said layer covering upper and side surfaces of the integrated circuit chip and peripheral side surfaces of the under-filling.

Claim 33 (Currently Amended): A unit for reading information from and writing information to a recording medium, comprising:

a head assembly having a mounting surface, a head slider provided with a head and mounted on the mounting surface, and an integrated circuit chip which is mounted on the mounting surface and processes information read from and/or written to the recording medium via the head,

said integrated circuit chip being covered by a layer 2 μ m or thinner,

a height of the integrated circuit chip, including the layer, being lower than a height of the head slider from the mounting surface.

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of **October 2, 2003**

D4
waif
Claim 34 (Previously Presented): The head assembly according to claim 1, wherein the mounting surface is substantially flat.

Claim 35 (Previously Presented): The head assembly according to claim 8, wherein the mounting surface is substantially flat.

Claim 36 (Currently Amended): A head assembly comprising:
a mounting surface;
an integrated circuit chip which is mounted so as to be raised above the mounting surface and processes signals; and
a head slider provided with a head,
said integrated circuit chip being covered at least on ~~the~~ corner portions and bumps mounting the integrated circuit chip raised above the mounting surface by a layer of poly(p-xylylene) 2 μ m or thinner,
wherein a height of the integrated circuit chip, including the layer, being lower than a height of the head slider from the mounting surface and the layer of poly(p-xylylene) prevents foreign particles from separating from the integrated circuit chip and interfering with the head assembly.

Claim 37 (Currently Amended): A disk unit for reading information from and writing

U.S. Patent Application Serial No. 09/548,313
Amendment dated February 2, 2004
Reply to OA of **October 2, 2003**

information to a disk, comprising:

D4
Concl.
a head assembly having a mounting surface, a head slider provided with a head, and an integrated circuit chip which is mounted so as to be raised above the mounting surface and processes information read from and/or written to the disk via the head,

said integrated circuit chip being covered at least on ~~the~~ corner portions and bumps mounting the integrated circuit chip raised above the mounting surface by a layer of poly(p-xylylene) 2 μ m or thinner,

wherein a height of the integrated circuit chip, including the layer, being lower than a height of the head slider from the mounting surface and the layer of poly(p-xylylene) prevents foreign particles from separating from the integrated circuit chip and interfering with the head assembly thereby preventing a crash of the disk unit.
